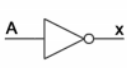


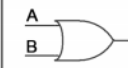

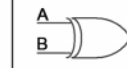
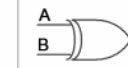
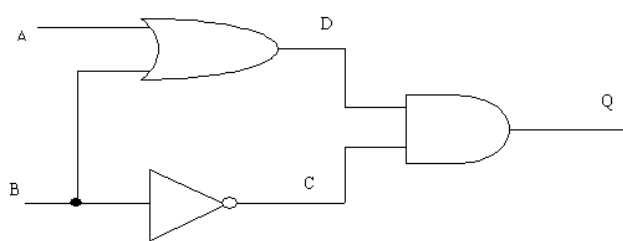


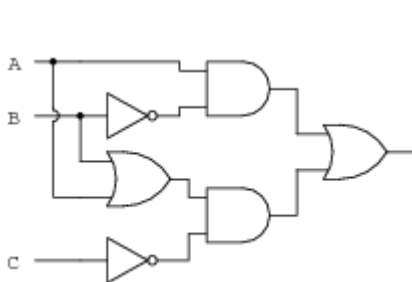
## Les portes logiques

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<b>Symbol</b>																																																																																																							
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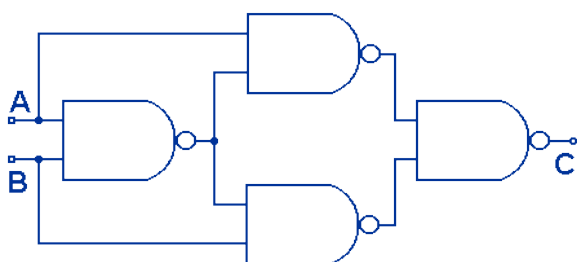
### Exercices



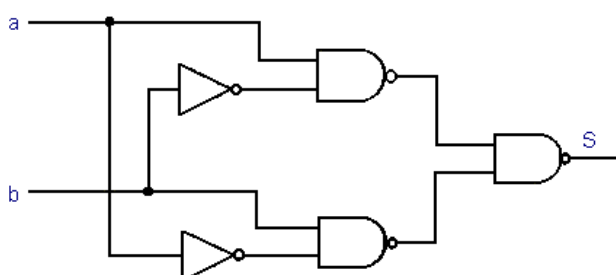
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B	A	X
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B	A	X
0	0	
0	1	
1	0	
1	1	



B	A	X
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0	1	
1	0	
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Fig. 50. - Schéma constitué de NAND et d'inverseurs.